



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD  
KUKATPALLY - HYDERABAD - 500 085  
EXAMINATION BRANCH

MOU-Foreign Collaborated Integrated Engineering Programme (USA)

SR INTERNATIONAL INSTITUTE OF TECHNOLOGY (VF) (MOU)

B.TECH II -II SEMESTER- R14- REGULAR AND SUPPLEMENTARY EXAMINATIONS, MARCH/APRIL- 2019

TIME TABLE

TIME: 10-00 AM TO 01-00 PM

BRANCH	DATE & DAY					
	30-03-2019 SATURDAY	02-04-2019 TUESDAY	04-04-2019 THURSDAY	09-04-2019 TUESDAY	11-04-2019 THURSDAY	15-04-2019 MONDAY
ELECTRONICS AND COMMUNICATIONS ENGINEERING	NETWORK THEORY	ELECTRONICS CIRCUIT	PROBABILITY THEORY AND STOCHASTIC PROCESSES	PRINCIPLES OF ELECTRICAL ENGINEERING	DIGITAL DESIGN USING VERILOG HDL	-----
COMPUTER SCIENCE AND ENGINEERING	COMPUTERS ORGANIZATION	SOFTWARE ENGINEERING	OBJECT ORIENTED PROGRAMMING	DATA BASE MANAGEMENT SYSTEMS	FORMAL LANGUAGES AND AUTOMATA THEORY	DESIGN AND ANALYSIS OF ALGORITHMS

NOTE:

- i. ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE CONTROLLER OF EXAMINATIONS, IMMEDIATELY.
- ii. EVEN IF GOVERNMENT DECLARES HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATIONS SHALL BE CONDUCTED AS USUAL.

DATE: 01-03-2019

  
CONTROLLER OF EXAMINATIONS



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD  
KUKATPALLY - HYDERABAD - 500 085  
EXAMINATION BRANCH

MOU-Foreign Collaborated Integrated Engineering Programme (USA)

SR INTERNATIONAL INSTITUTE OF TECHNOLOGY (VF)

B.TECH III YEAR - II SEMESTER- R14- REGULAR EXAMINATIONS, MARCH/APRIL- 2019  
TIME TABLE

TIME: 10-00 AM TO 1-00 PM

BRANCH	DATE, TIME & DAY					
	29-03-2019 FRIDAY	01-04-2019 MONDAY	03-04-2019 WEDNESDAY	08-04-2019 MONDAY	10-04-2019 WEDNESDAY	12-04-2019 FRIDAY
COMPUTER SCIENCE AND ENGINEERING (33-C S E)	DISTRIBUTED SYSTEMS	PRINCIPLES OF PROGRAMMING LANGUAGES	COMPLIER DEDIGN	INFORMATION SECURITY	OBJECT ORIENTED ANALYSIS AND DESIGN	WEB TECHNOLOGIES
ELECTRONICS AND COMMUNICATIONS ENGINEERING (32-E C E)	COMPUTER ORGANIZATION	DIGITAL COMMUNICATIONS	VLSI DESIGN	ANTENNAS AND WAVE PROPAGATION	CONTROL SYSTEMS ENGINEERING	DIGITAL SIGNAL PROCESSING

NOTE:

- ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE CONTROLLER OF EXAMINATIONS, IMMEDIATELY.
- EVEN IF GOVERNMENT DECLARES HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATIONS SHALL BE CONDUCTED AS USUAL.

DATE: 01-03-2019

*S. Pan Kelly*  
CONTROLLER OF EXAMINATIONS



**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD**  
KUKATPALLY - HYDERABAD - 500 085  
EXAMINATION BRANCH

**MOU-Foreign Collaborated Integrated Engineering Programme (USA)**

SR INTERNATIONAL INSTITUTE OF TECHNOLOGY (VF)

B.TECH III YEAR - II SEMESTER- R13- SUPPLEMENTARY EXAMINATIONS, MARCH/APRIL- 2019  
TIME TABLE

TIME: 10-00 AM TO 1-00 PM

BRANCH	DATE, TIME & DAY					
	29-03-2019 FRIDAY	-----	----	----	----	----
ELECTRONICS AND COMMUNICATIONS ENGINEERING (32-ECE)	DIGITAL COMMUNICATIONS	----	----	----	----	----

- NOTE:**
- i. ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE CONTROLLER OF EXAMINATIONS, IMMEDIATELY.
  - ii. EVEN IF GOVERNMENT DECLARES HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATIONS SHALL BE CONDUCTED AS USUAL.

DATE: 01-03-2019

*S. Tan Kalyan*  
CONTROLLER OF EXAMINATIONS



**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD**  
KUKATPALLY - HYDERABAD - 500 085  
EXAMINATION BRANCH

**MOU-Foreign Collaborated Integrated Engineering Programme (USA)**

SR INTERNATIONAL INSTITUTE OF TECHNOLOGY (VF)

B.TECH III YEAR - II SEMESTER - R09 - SUPPLEMENTARY EXAMINATIONS, MARCH/APRIL - 2019

TIME TABLE

TIME: 10-00 AM TO 1-00 PM

BRANCH	DATE, TIME & DAY					
	29-03-2019 FRIDAY	-----	-----	-----	-----	-----
ELECTRONICS AND COMMUNICATIONS ENGINEERING (32-E C E)	DIGITAL SIGNAL PROCESSING	-----	-----	-----	-----	-----

**NOTE:**

- i. ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE CONTROLLER OF EXAMINATIONS, IMMEDIATELY.
- ii. EVEN IF GOVERNMENT DECLARES HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATIONS SHALL BE CONDUCTED AS USUAL.

DATE: 01-03-2019

*S. Tan kely*  
CONTROLLER OF EXAMINATIONS

*POC*